



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

## BIB DATA SHEET

CONFIRMATION NO. 7114

<b>SERIAL NUMBER</b> 10/595,303	<b>FILING or 371(c) DATE</b> 05/19/2006 <b>RULE</b>	<b>CLASS</b> 438	<b>GROUP ART UNIT</b> 2823	<b>ATTORNEY DOCKET NO.</b> 60291.000048		
<b>APPLICANTS</b> Roy Knechtel, Geraberg, GERMANY; /K.P./ <b>** CONTINUING DATA *****</b> This application is a 371 of PCT/DE04/02413 10/29/2004 /K.P./ <b>** FOREIGN APPLICATIONS *****</b> GERMANY 103 50 460.5 10/29/2003 /K.P./ <b>** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **</b> 08/22/2007						
Foreign Priority claimed <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No 35 USC 119(a-d) conditions met <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No Verified and Acknowledged <u>/KEVIN A PAREDO/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance Initials	<b>STATE OR COUNTRY</b> GERMANY	<b>SHEETS DRAWINGS</b> 2	<b>TOTAL CLAIMS</b> 20	<b>INDEPENDENT CLAIMS</b> 3
<b>ADDRESS</b> HUNTON & WILLIAMS LLP INTELLECTUAL PROPERTY DEPARTMENT 1900 K STREET, N.W. SUITE 1200 WASHINGTON, DC 20006-1109 UNITED STATES						
<b>TITLE</b> Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers						
<b>FILING FEE RECEIVED</b> 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		